



Please type a plus sign (+) inside this box

PTO/SB/81 (11/96)
Approved for use through 6/30/99. OMB 0651-0035
Patent and Trademark Office: U.S. DEPARTMENT OF

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**POWER OF ATTORNEY OR
AUTHORIZATION OF AGENT**

Application Number	10/626,366
Filing Date	July-23, 2003
First Named Inventor	Hong Lim
Group Art Unit	2811
Examiner Name	Nitin Parekh
Attorney Docket Number	LSI1P225 / 03-0466

BEST AVAILABLE COPY

I hereby appoint:

☒ Practitioners at Customer Number
OR

24319

Place Customer
Number Bar Code
Label Here☒ Practitioner(s) named below:

Name	Registration Number
PETE P. SCOTT	33,279
SANDEEP JAGGI	43,331
TIMOTHY R. CROLL	36,771
LEO J. PETERS	33,562

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the Patent and Trademark Office connected therewith.

This communication is NOT intended to revoke any existing powers of attorney or authorizations of agent.

Please change the correspondence address for the above-identified application to:

☐ The above mentioned Customer Number
OR

<input checked="" type="checkbox"/> Firm or Individual Name	LSI LOGIC CORPORATION					
Address	1621 Barber Lane., MS: D-106					
Address	Patent Law Department					
City	MILPITAS	State	CA	ZIP	95035	
Country	USA					
Telephone	408-433-7475		Fax	408-433-7460		

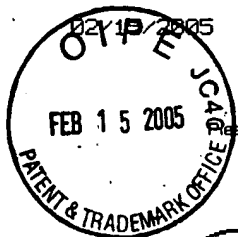
I am the:

☐ Applicant☒ Assignee of Record of the entire interest

Certificate under 37 CFR 3.73(b) is enclosed

SIGNATURE of Applicant or Assignee of Record

Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.
Signature	
Date	2/14/05



Please type a plus sign (+) inside this box →

PTO/SB/122 (11/96)
Approved for use through 6/30/99. OMB 0651-0035
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**CHANGE OF
CORRESPONDENCE ADDRESS
Application**Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Application Number	10/626,366
Filing Date	July-23, 2003
First Named Inventor	Hong Lim
Group Art Unit	2811
Examiner Name	Nitin Parekh
Attorney Docket Number	LSI1P225 / 03-0488

Please change the Correspondence Address for the above-identified application to:

☒ Customer Number24319
Type Customer Number herePlace Customer
Number Bar Code
Label Here

OR

<input checked="" type="checkbox"/> Firm or Individual Name	LSI LOGIC CORPORATION				
Address	1621 Barber Lane, MS: D-106				
Address	Patent Law Department				
City	MILPITAS	State	CA	ZIP	95035
Country	USA				
Telephone	408-433-7475	Fax	408-433-7460		

This form cannot be used to change the data associated with a Customer Number. To change data associated with an existing Customer Number use "Request for Customer Number Data Change" (PTO/SB/124).

I am the:

☐ Applicant☒ Assignee of Record of the entire interest
Certificate under 37 CFR 3.73(b) is enclose☐ Attorney or agent of record.**SIGNATURE of Applicant or Assignee of Record**

Typed or Printed Name	SANDEEP JAGGI, Chief IP Counsel, Assistant Corporate Secretary, LSI Logic
Signature	
Date	2-14-05

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO : Assistant Commissioner for Patents, Washington, DC 20231.

BEST AVAILABLE COPY



02/15/2005

15:32

LSI LOGIC LEGAL IP → 917037464000

NO.394

0008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re Application of :**

Hong Lim et. al.

Serial No. :

10/626,366

Group Art Un 2811**Filed :**

July 23, 2003

Examiner: Parekh, Nitin**For :**

Integrated Circuit Package and
Method for a PBGA Package
Having a Multiplicity of Staggered
Power Ring Segments For Power
Connection to Integrated Circuit Die

Attv Docket : LSI1P225 / 03-0466

I hereby certify that this correspondence is being deposited with the U.S.
Postal Service as First Class Mail in an envelope addressed to:
Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450,
on the date below:

Connie Del Castillo

2/15/05 *Connie del Castillo*

Date

Signature

BEST AVAILABLE COPY

CERTIFICATION UNDER 37 C.F.R. 3.73(b)

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
(408) 433-7475

Date:

2-14-05

Respectfully submitted,

Sandeep Jaggi
Sandeep Jaggi
Chief IP Counsel
Corporate Assistant Secretary
LSI LOGIC CORPORATION